PATENT ABSTRACTS OF JAPAN

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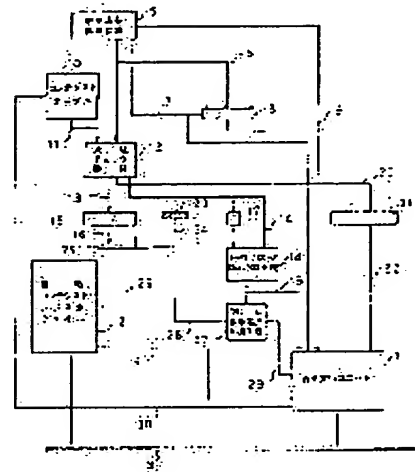(22)Date of filing :   07.06.1989   (72)Inventor : FUJITA TETSUYA

(54) INTERRUPTION PROCESSING SYSTEM

(57)Abstract:
PURPOSE: To reduce the saving/restoring overhead of a context at the time of interruption processing while suppressing the increment of hardwares by storing the context of a part of an interruption level defined by a processor in the processor.
CONSTITUTION: When an instruction circuit means 18 instructs a frame number selection control means 27 and an instruction execution unit 1 to restore a context from a memory area, the means 27 instructs a selection means 25 to select a frame number for a system mode. A program for saving/restoring contexts extracts a context from the memory in accordance with a context storing address 21 and loads the context. Consequently, overhead saving/restoring the context at the time of generating an interruption can be removed.

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